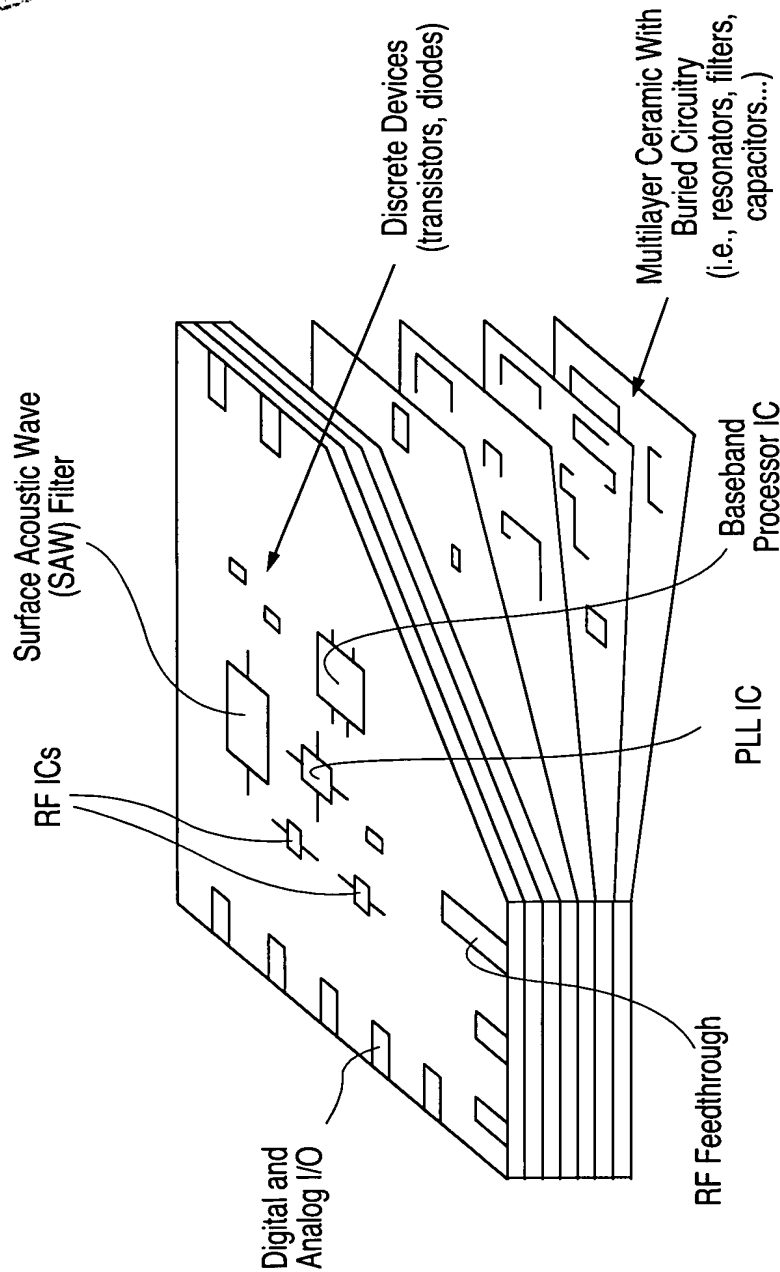


Buried Self-Resonant Bypass Capacitors Within Multilayered  
Low Temperature Co-Fired Ceramic (LTCC) Substrate

Inventor: Christopher Barratt

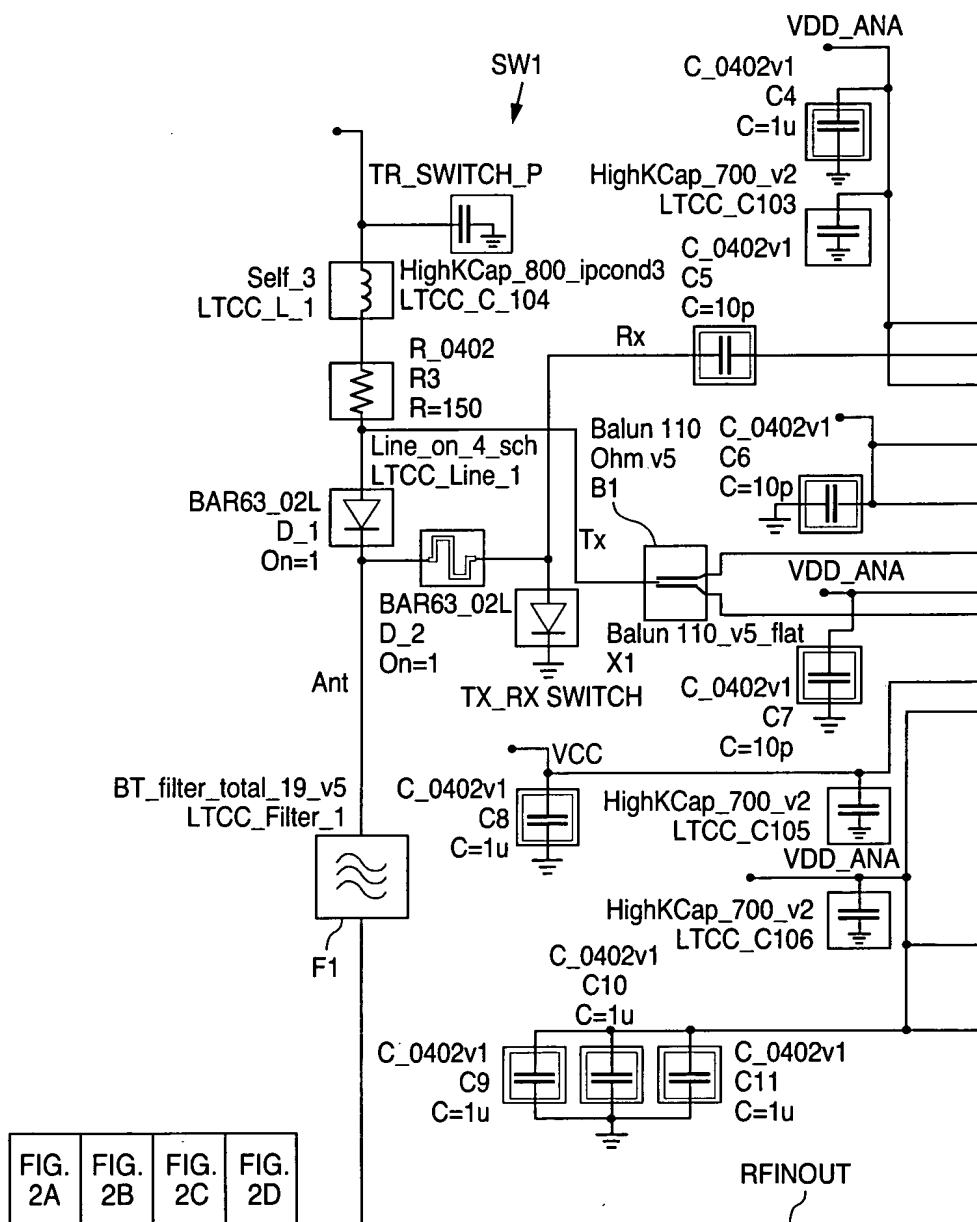
Application No.: 10/675,698



**FIG. 1**  
(PRIOR ART)

Buried Self-Resonant Bypass Capacitors Within Multilayered  
Low Temperature Co-Fired Ceramic (LTCC) Substrate  
Inventor: Christopher Barratt  
Application No.: 10/675,698

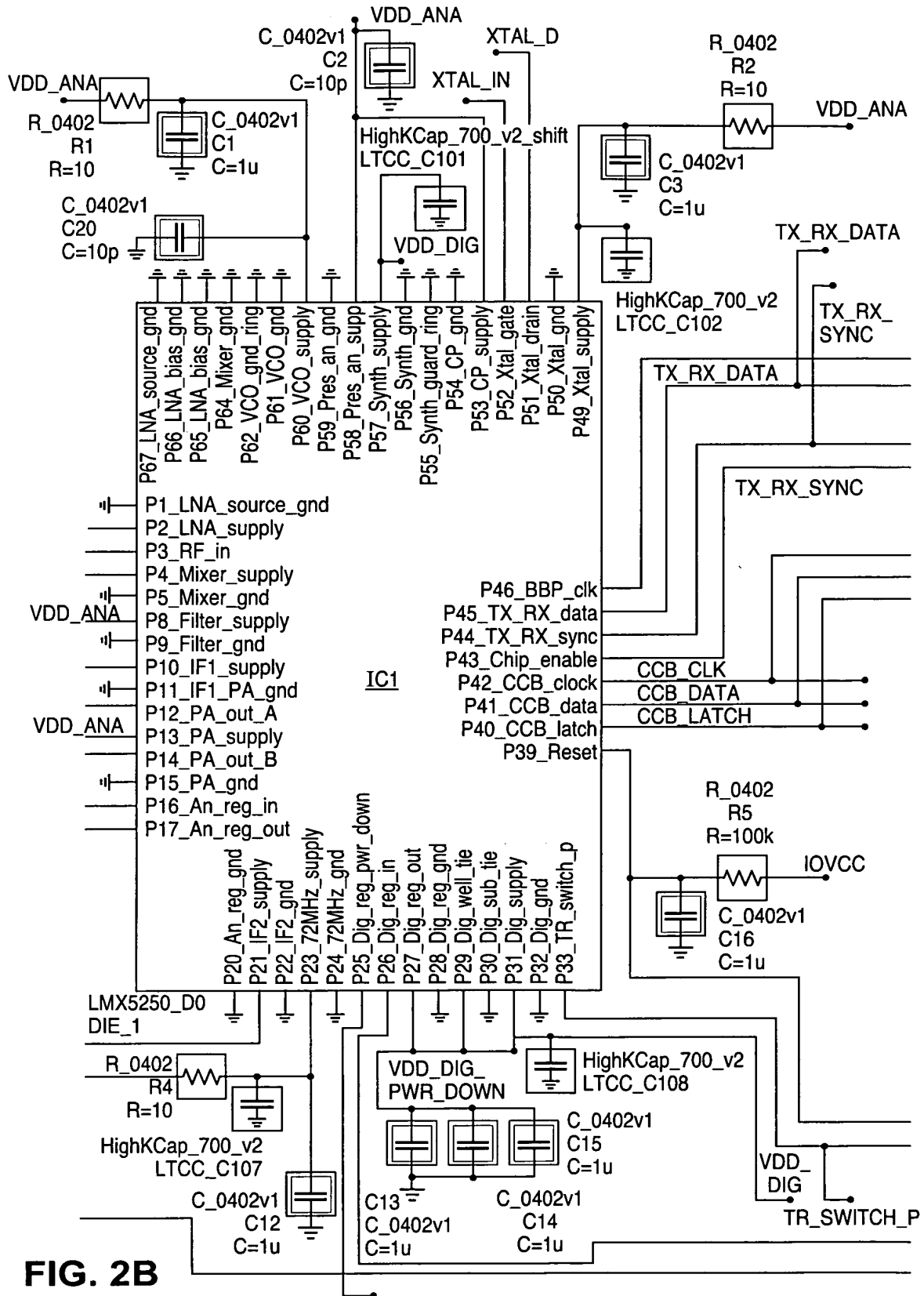
**FIG. 2A**



**Buried Self-Resonant Bypass Capacitors Within Multilayered  
Low Temperature Co-Fired Ceramic (LTCC) Substrate**

**Inventor: Christopher Barratt**

**Application No.: 10/675,698**



**FIG. 2B**

**Inventor: Christopher Barratt**  
**Application No.: 10/675,698**



Buried Self-Resonant Bypass Capacitors Within Multilayered  
Low Temperature Co-Fired Ceramic (LTCC) Substrate

Inventor: Christopher Barratt

Application No.: 10/675,698

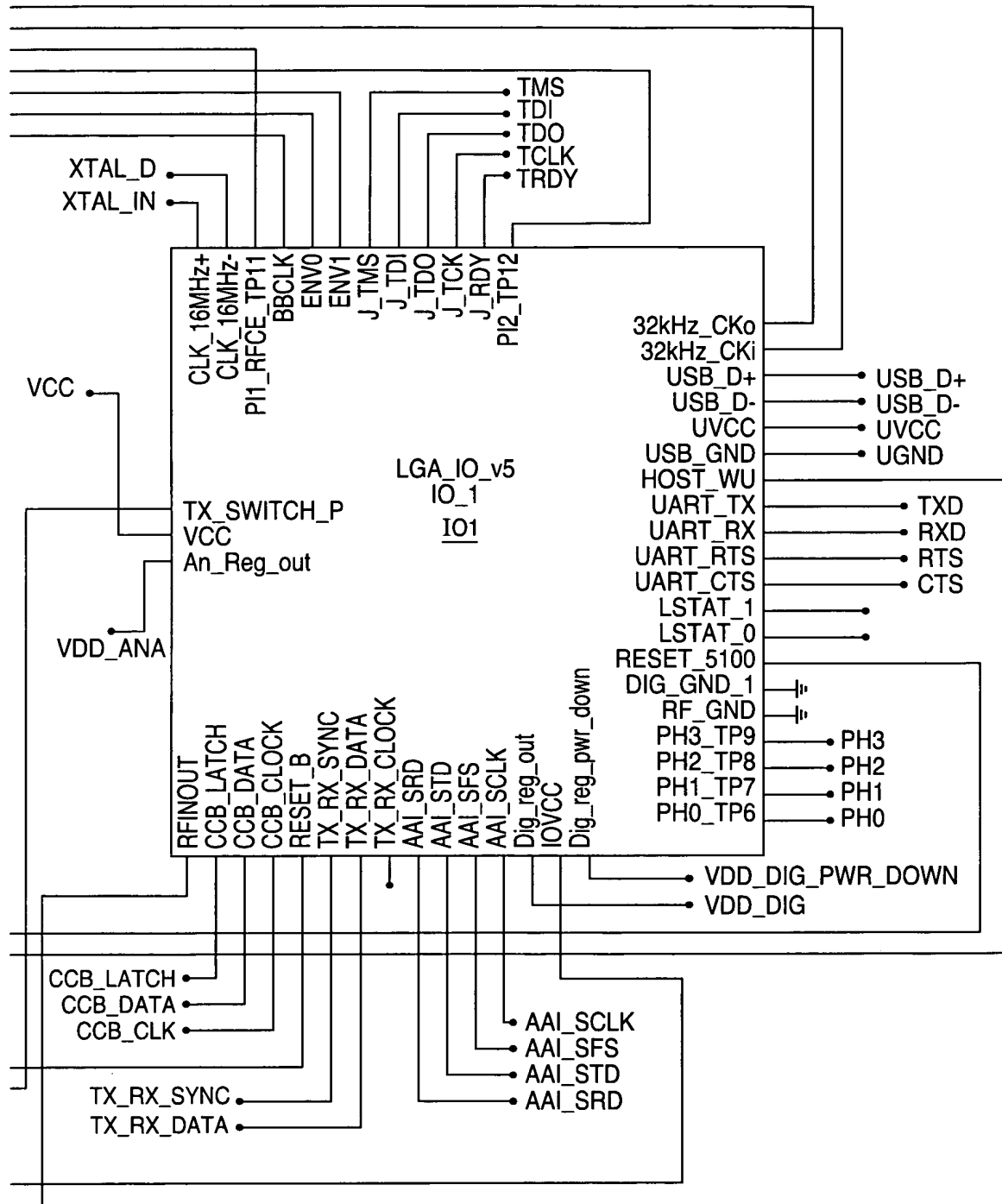


FIG. 2D

**Inventor: Christopher Barratt**  
**Application No.: 10/675,698**



**FIG. 3**

Buried Self-Resonant Bypass Capacitors Within Multilayered  
Low Temperature Co-Fired Ceramic (LTCC) Substrate

Inventor: Christopher Barratt

Application No.: 10/675,698

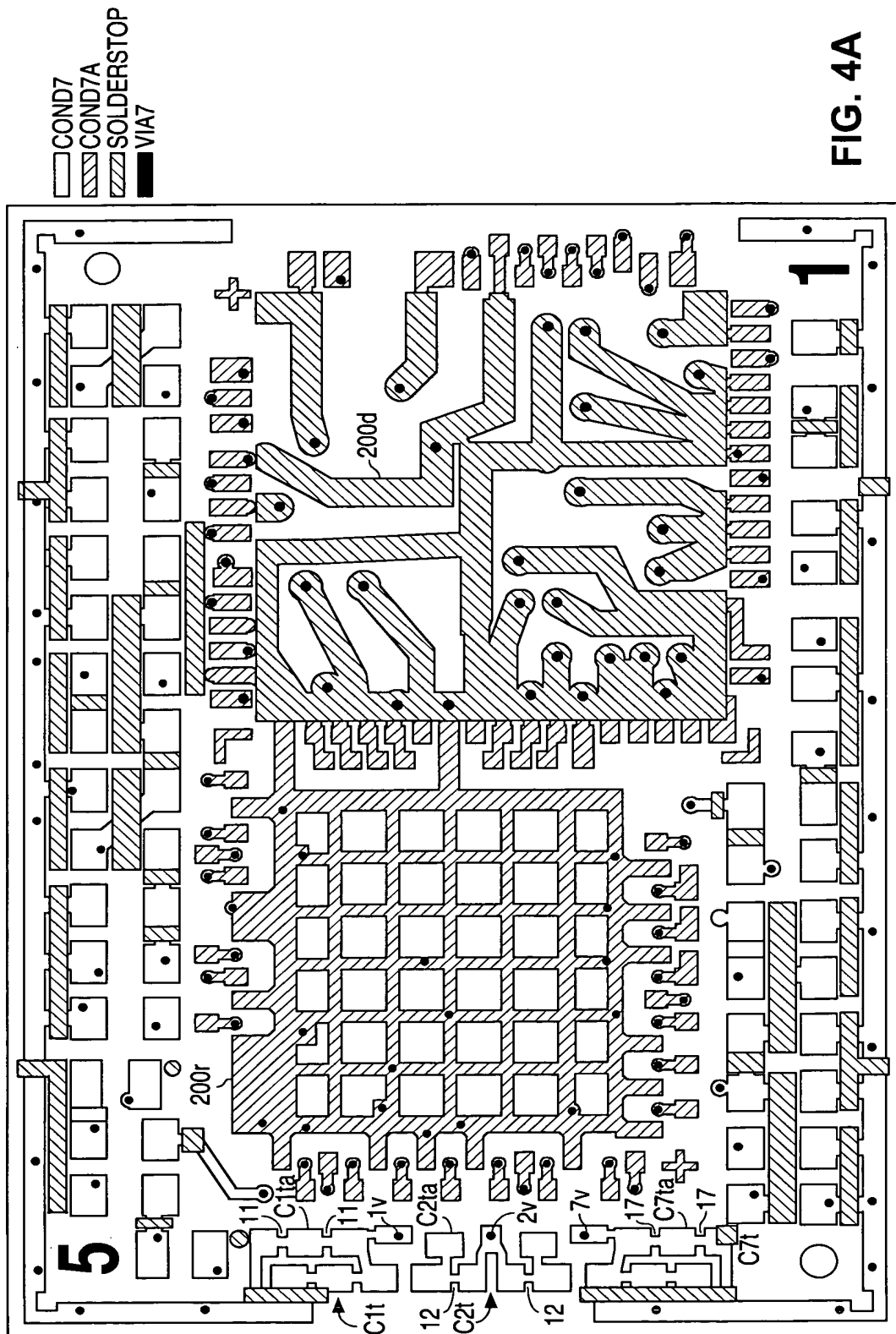


FIG. 4A

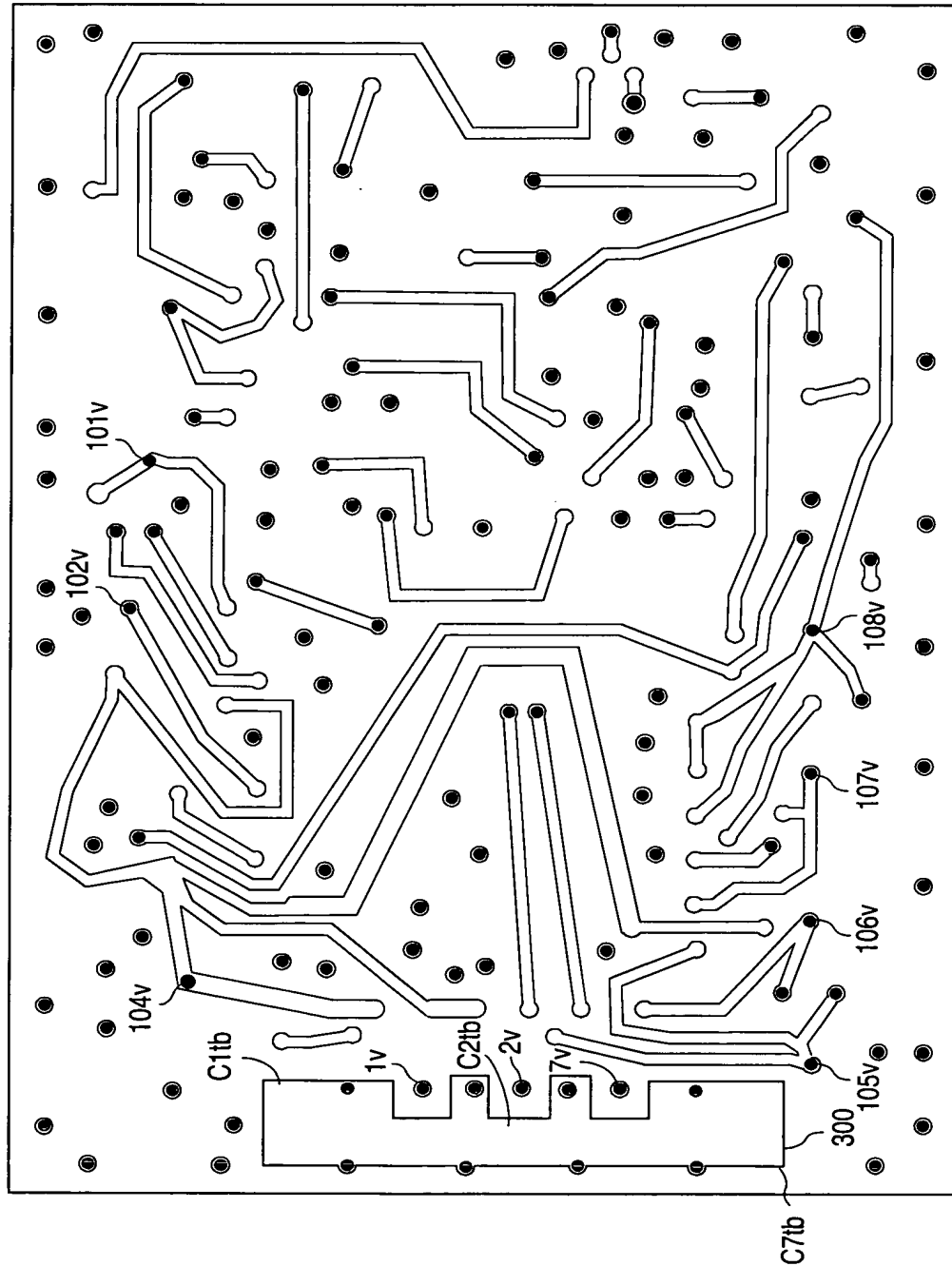
Buried Self-Resonant Bypass Capacitors Within Multilayered  
Low Temperature Co-Fired Ceramic (LTCC) Substrate

Inventor: Christopher Barratt

Application No.: 10/675,698

COND6  
VIA6

FIG. 4B





Buried Self-Resonant Bypass Capacitors Within Multilayered  
Low Temperature Co-Fired Ceramic (LTCC) Substrate

Inventor: Christopher Barratt

Application No.: 10/675,698

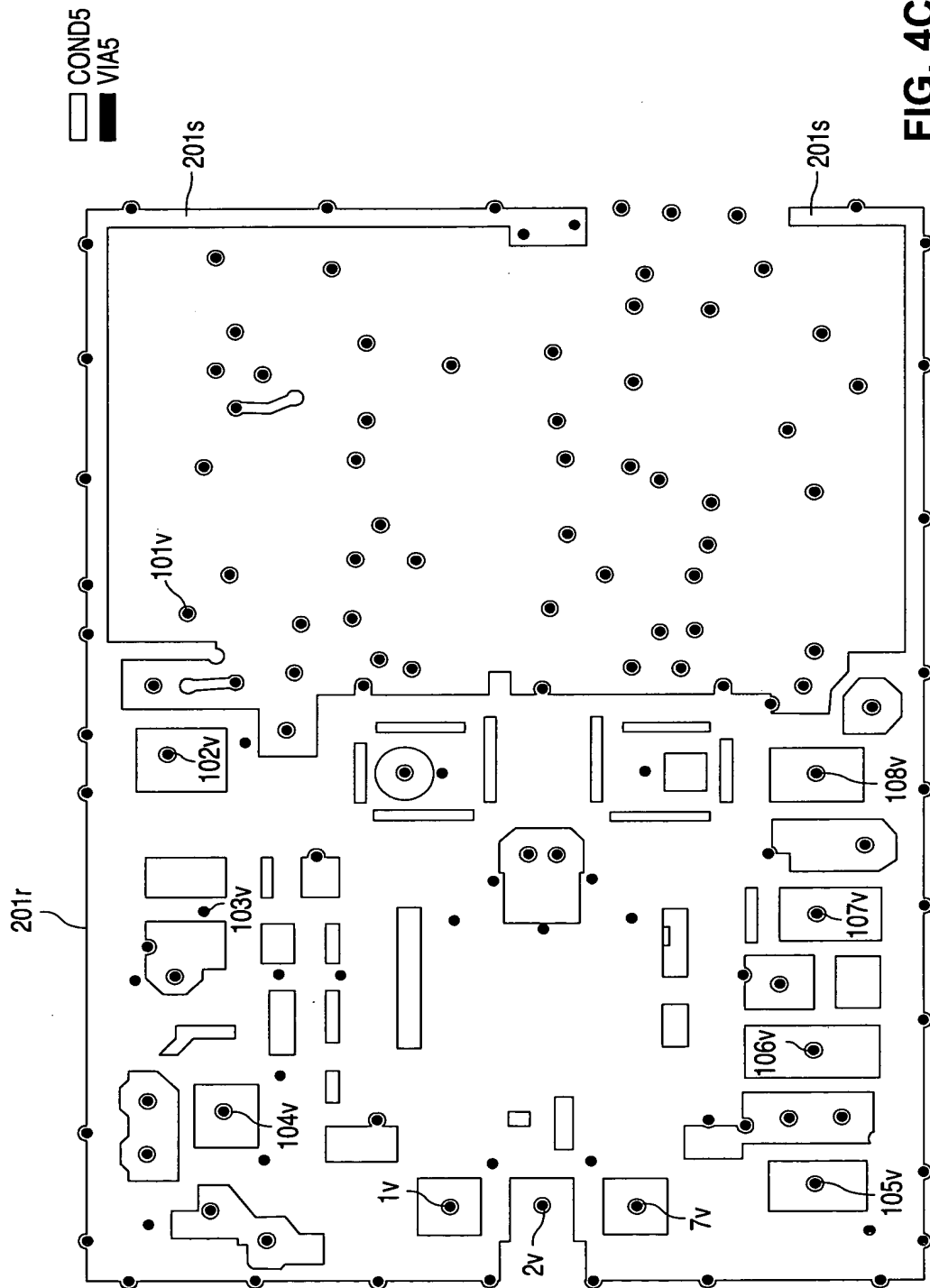
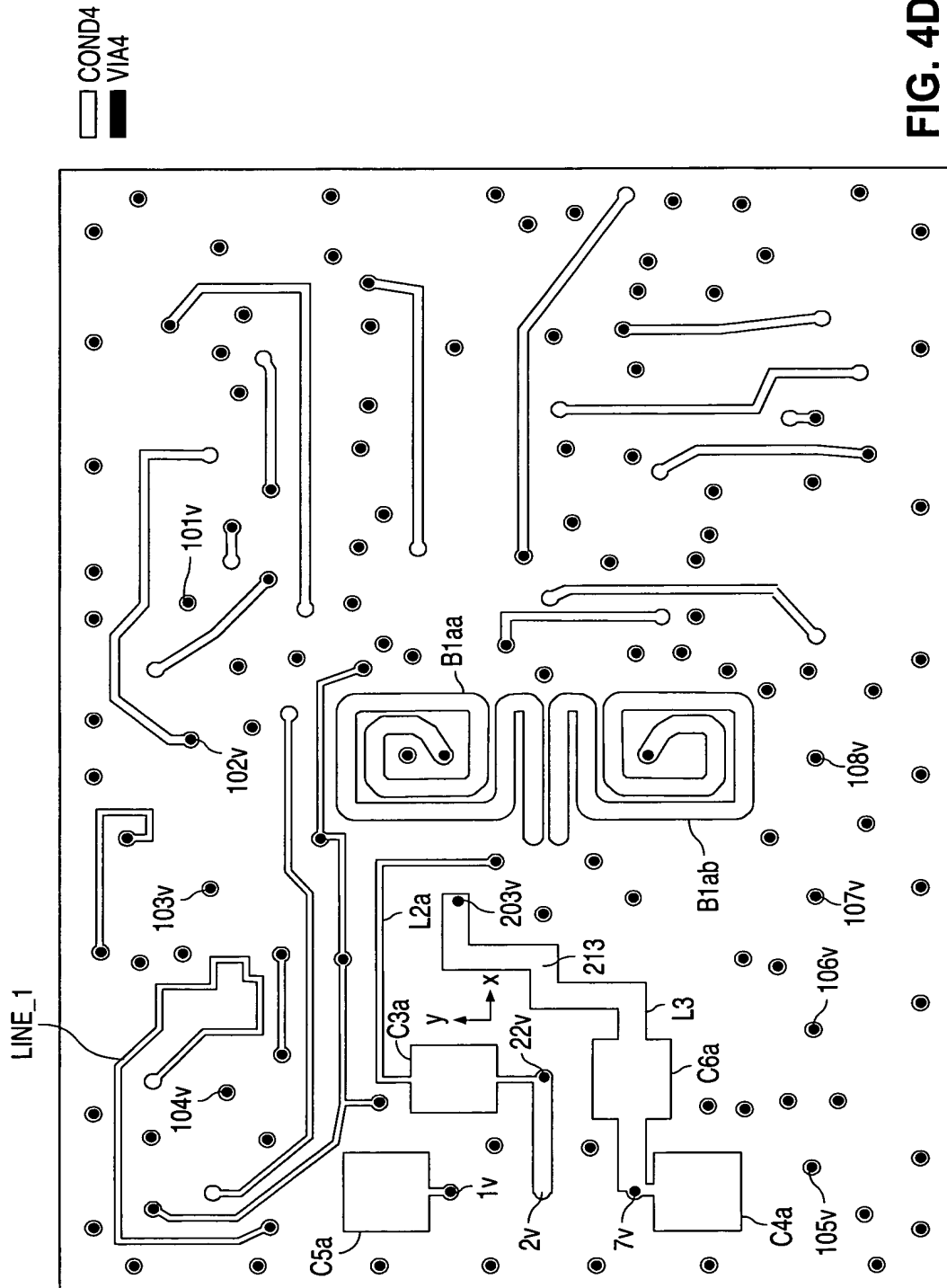


FIG. 4C

Buried Self-Resonant Bypass Capacitors Within Multilayered  
Low Temperature Co-Fired Ceramic (LTCC) Substrate

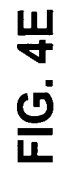
Inventor: Christopher Barratt

Application No.: 10/675,698



**Inventor: Christopher Barratt**  
**Application No.: 10/675,698**

**Inventor: Christopher Barratt**  
**Application No.: 10/675,698**



**FIG. 4E**

Buried Self-Resonant Bypass Capacitors Within Multilayered  
Low Temperature Co-Fired Ceramic (LTCC) Substrate

Inventor: Christopher Barratt

Application No.: 10/675,698

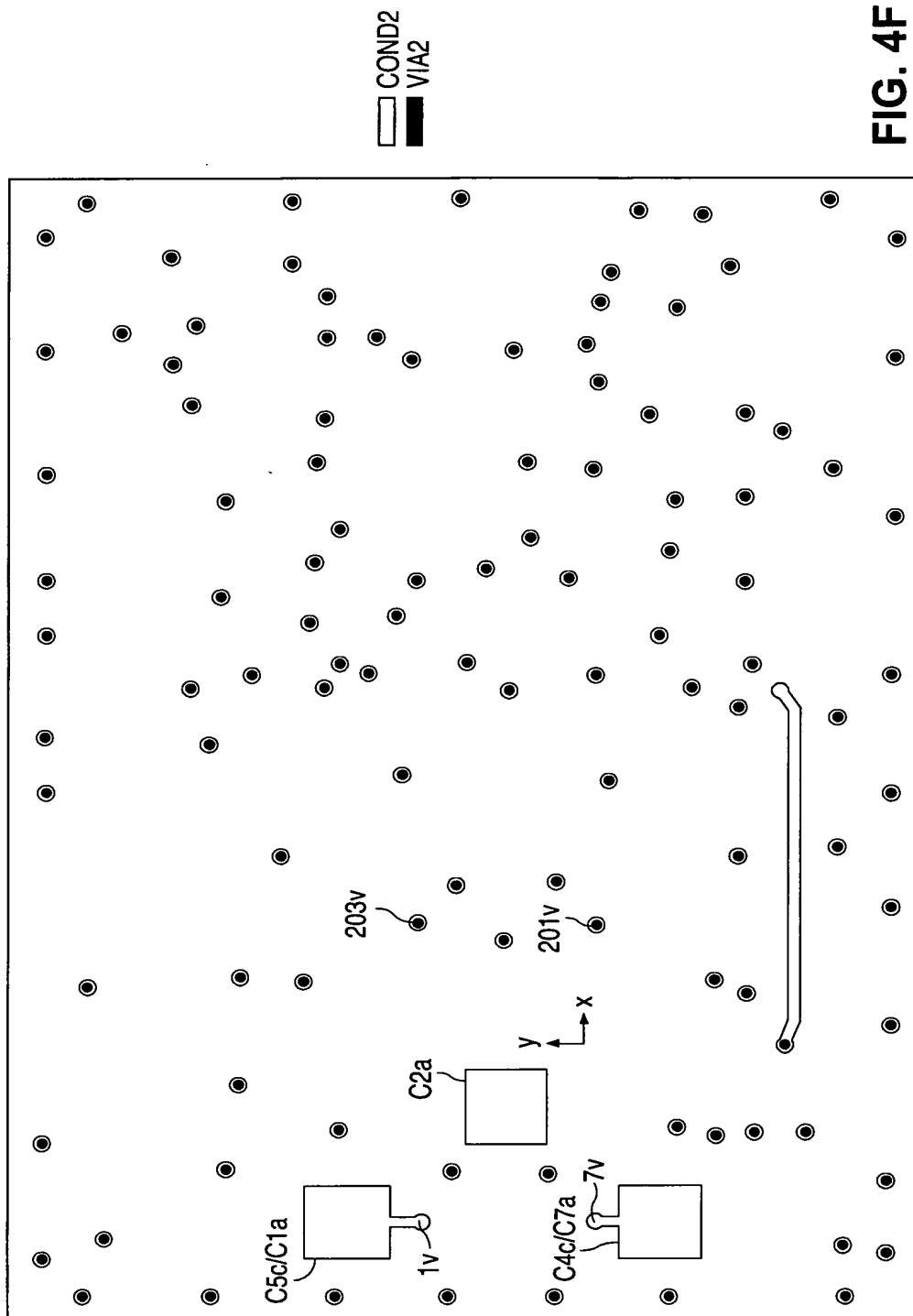


FIG. 4F

Buried Self-Resonant Bypass Capacitors Within Multilayered  
Low Temperature Co-Fired Ceramic (LTCC) Substrate

Inventor: Christopher Barratt

Application No.: 10/675,698

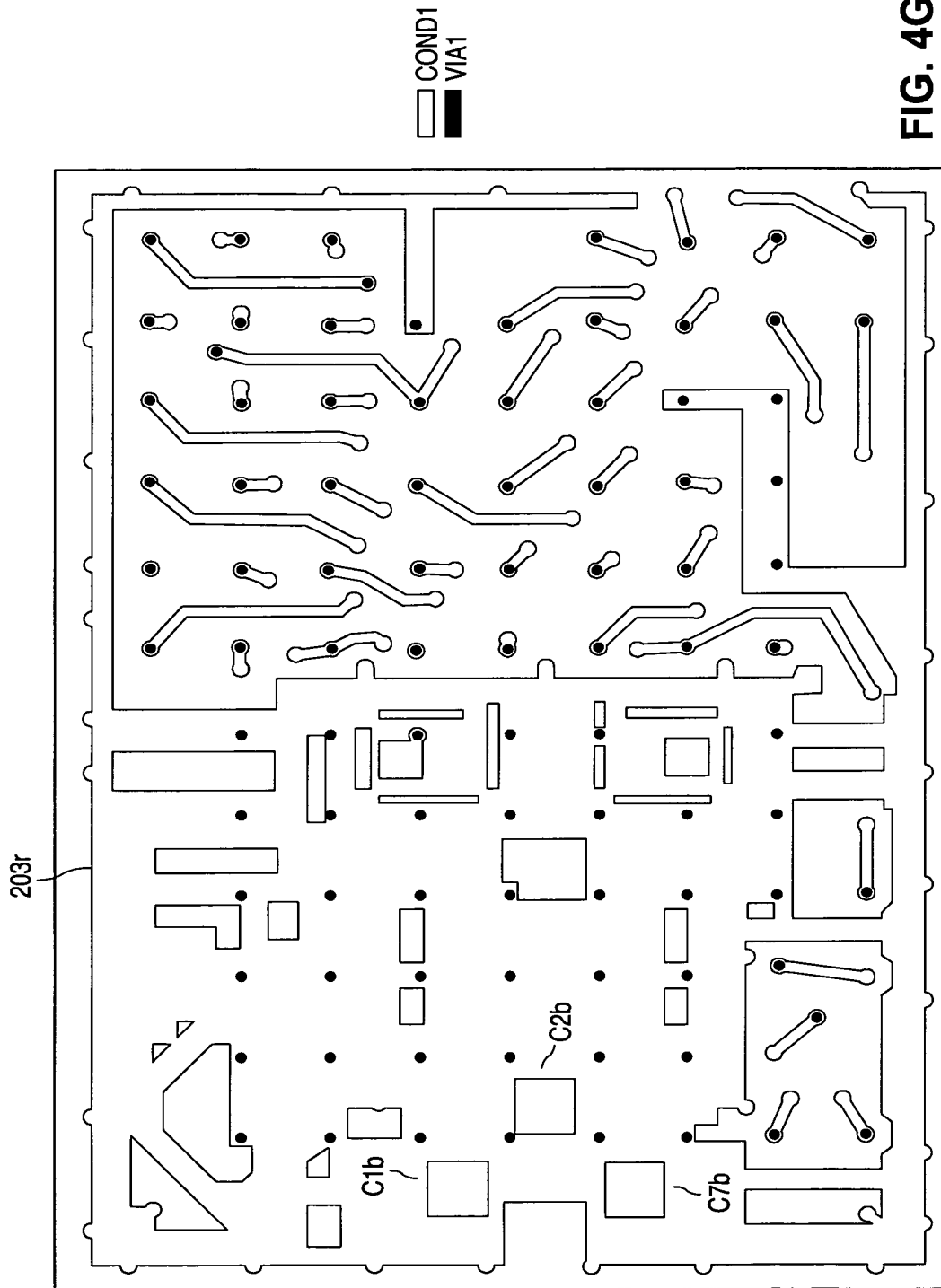
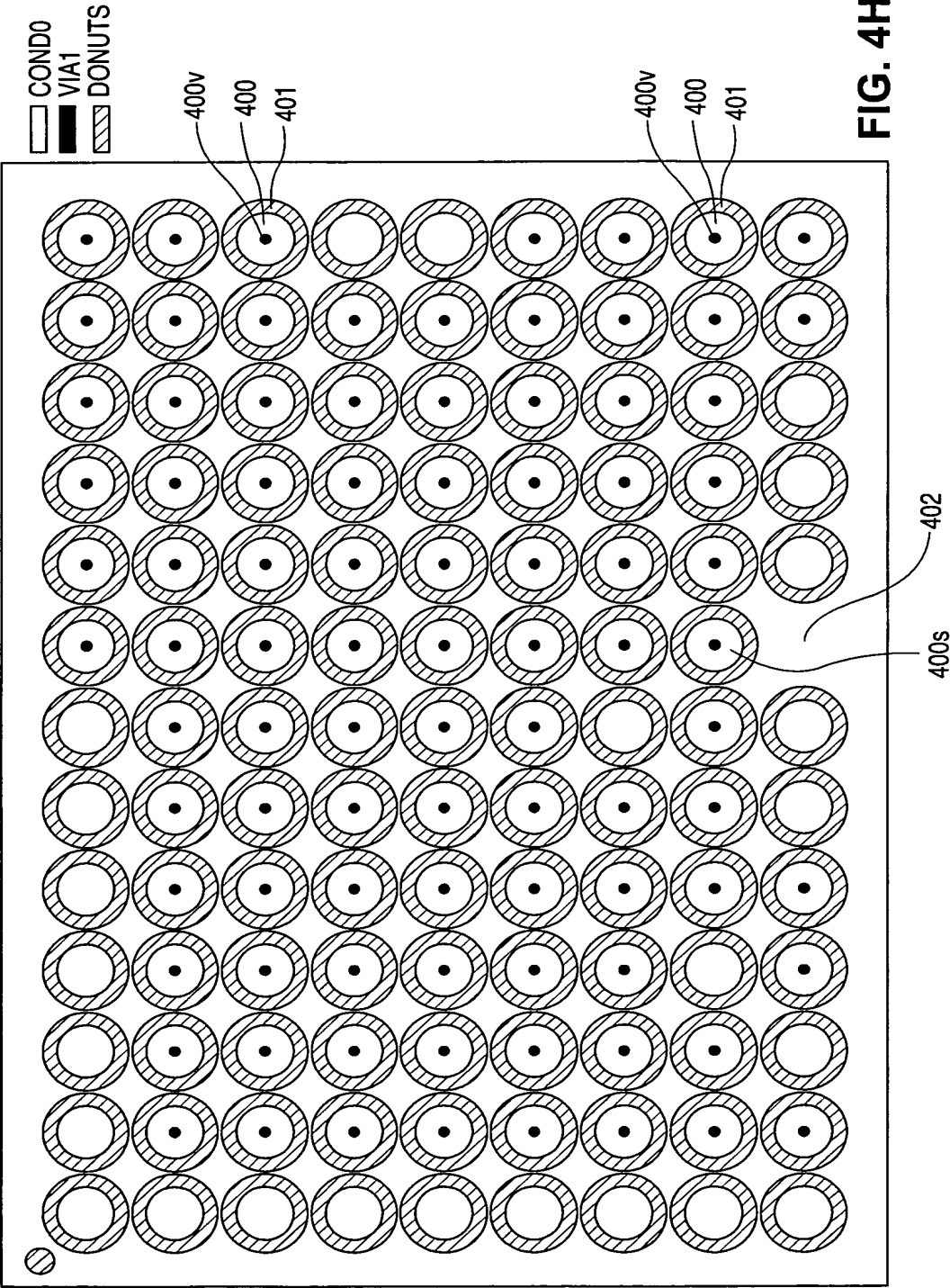


FIG. 4G

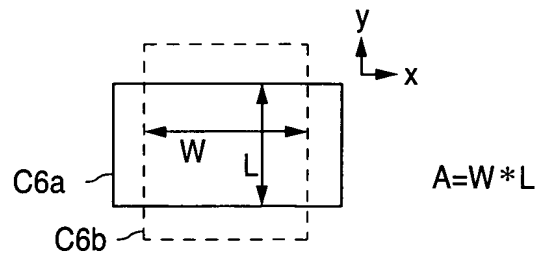
Buried Self-Resonant Bypass Capacitors Within Multilayered  
Low Temperature Co-Fired Ceramic (LTCC) Substrate  
Inventor: Christopher Barratt  
Application No.: 10/675,698



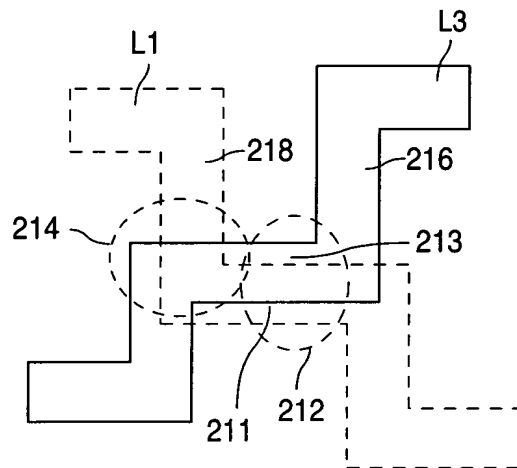
Buried Self-Resonant Bypass Capacitors Within Multilayered  
Low Temperature Co-Fired Ceramic (LTCC) Substrate

Inventor: Christopher Barratt

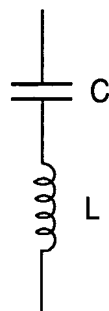
Application No.: 10/675,698



**FIG. 5**



**FIG. 6**



**FIG. 7**